

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	WIRING STRUCTURE FOR INTEGRATED CIRCUIT WITH REDUCED INTRALEVEL CAPACITANCE
Application Number :	
Date :	
First Named Applicant:	Richard S. Wise
Confirmation Number:	
Attorney Docket Number:	FIS920030028

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Submitted By:	Elec. Sign.	Sign. Capacity
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<b>Documents being submitted:</b>	<b>Files</b>
us-ids	FIS920030028-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	FIS920030028-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030028assign1.tif FIS920030028assign2.tif FIS920030028assign3.tif FIS920030028assign4.tif
us-request	FIS920030028-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920030028-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS920030028dec1.tif
us-declaration	FIS920030028dec2.tif
us-declaration	FIS920030028dec3.tif
us-declaration	FIS920030028dec4.tif
us-declaration	FIS920030028dec5.tif
us-declaration	FIS920030028dec6.tif
application-body	FIS920030028-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent

soextblk.dtd  
FIS920030028table.tif  
FIS920030028dwg1.tif  
FIS920030028dwg2.tif  
FIS920030028dwg3.tif

**Comments**

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